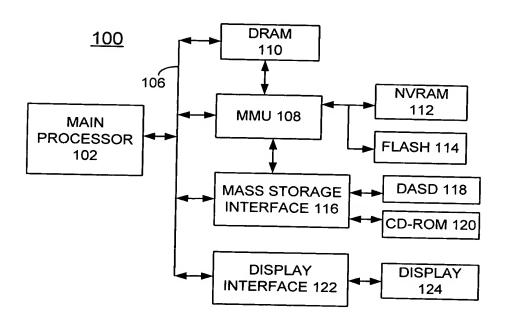
## FIG. 1A

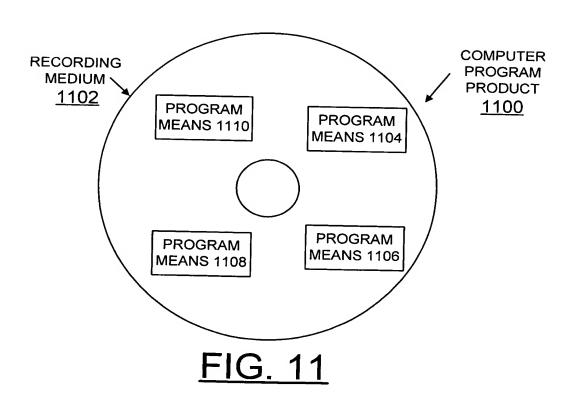


ELECTRONIC PACKAGE DESIGN PROGRAM 132

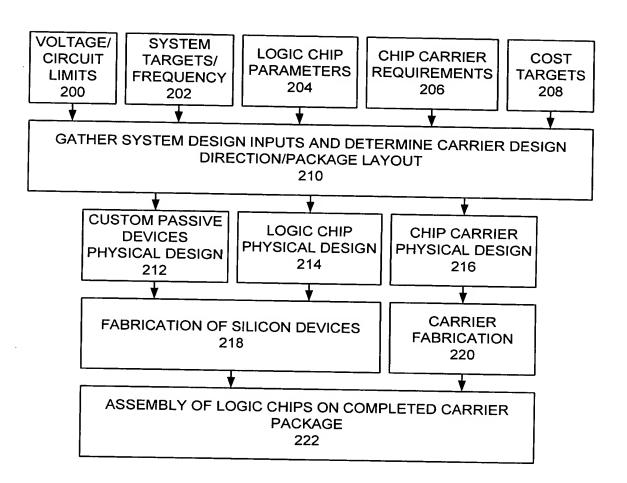
SILICON PASSIVE DEVICES CUSTOMIZING PROGRAM 134

USER INTERFACE 136

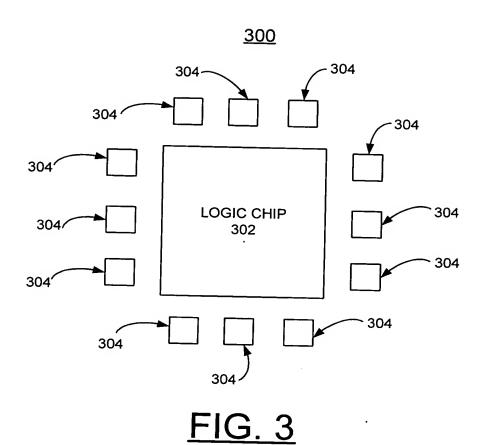
OPERATING SYSTEM 130

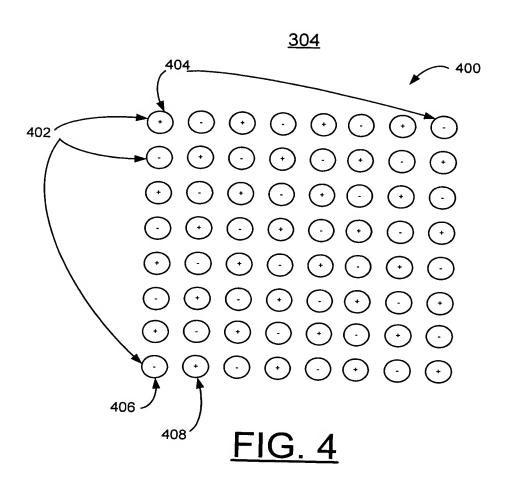


 $(X_{i}, \mathcal{O}, \mathcal{O}_{i}, \mathcal{O}_{i})$ 



**FIG. 2** 





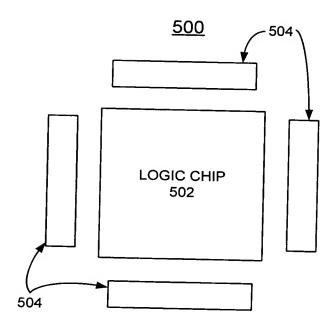
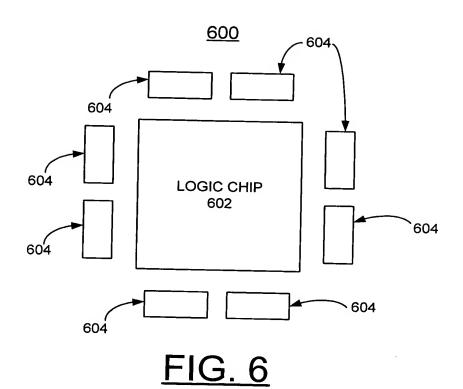
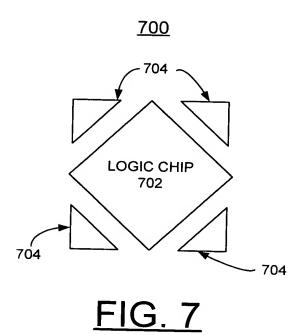


FIG. 5



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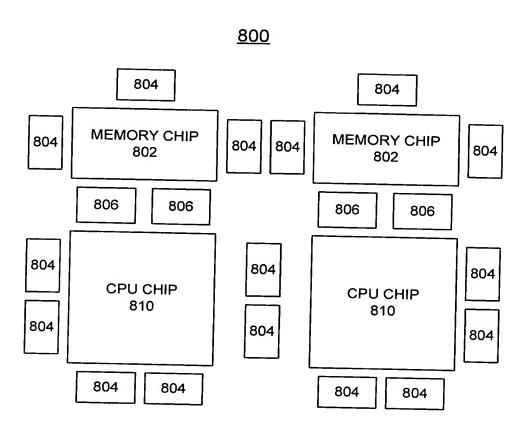


FIG. 8

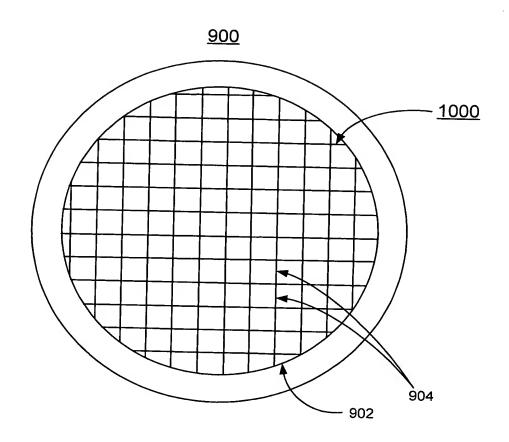


FIG. 9

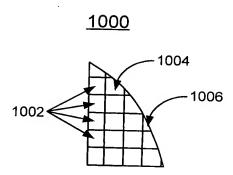


FIG. 10